TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOSIV)

2SK3742

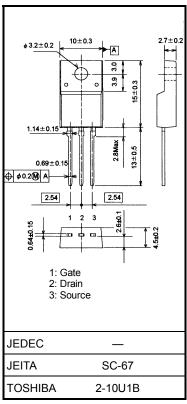
Switching Regulator Applications

Unit: mm

- Low drain-source ON resistance: RDS (ON) = 2.2 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 3.5 \text{ S (typ.)}$
- Low leakage current: IDSS = 100 $\,\mu$ A (VDS = 720 V)
- Enhancement model: $V_{th} = 4.0 \sim 5.0 \text{ V (V}_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	900	V	
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$	V_{DGR}	900	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	5	А	
	Pulse (t = 1 ms) (Note 1)	I _{DP}	15		
Drain power dissipati	on (Tc = 25°C)	PD	45	W	
Single pulse avalanche energy (Note 2)		E _{AS}	595	mJ	
Avalanche current		I _{AR}	5	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	4.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

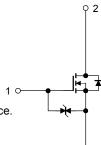


Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W



- Note 1: Ensure that the channel temperature does not exceed 150°C during use of the device.
- Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 43.6 mH, $I_{AR} = 5.0 \text{ A}$, $R_G = 25 \Omega$
- Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



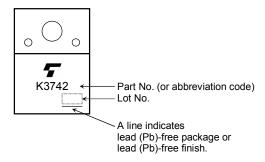
Electrical Characteristics (Ta = 25°C)

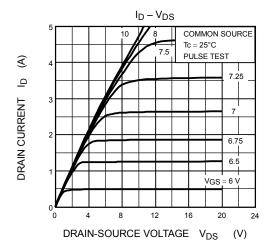
Char	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate-source brea	kdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	900	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	4.0	_	5.0	V
Drain-source ON	resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 3 A		2.2	2.5	Ω
Forward transfer	admittance	Y _{fS}	V _{DS} = 20 V, I _D = 3 A	1.5	3.5	_	S
Input capacitance		C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		1150	_	pF
Reverse transfer capacitance		C _{rss}			20	_	
Output capacitance		Coss	1		110	_	
Switching time	Rise time	t _r	V_{GS} $V_{DD} \simeq 200 \text{ V}$ $V_{DD} \simeq 200 \text{ V}$	_	100	_	
	Turn-on time	t _{on}		_	140		
	Fall time	t _f			40	-	ns
	Turn-off time	t _{off}	Duty \leq 1%, $t_W = 10 \mu s$		130	_	
Total gate charge		Qg		_	25	_	
Gate-source charge		Qgs	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	_	11	_	nC
Gate-drain charge		Q _{gd}			14	_	

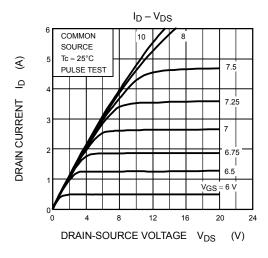
Source-Drain Ratings and Characteristics (Ta = 25°C)

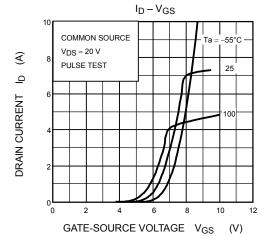
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	15	Α
Forward voltage (diode)	V _{DSF}	$I_{DR} = 5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 5 \text{ A}, V_{GS} = 0 \text{ V},$	_	900	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	5.4	_	μС

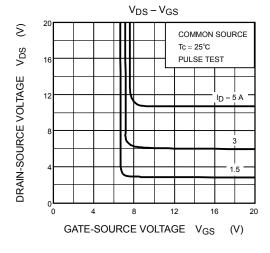
Marking

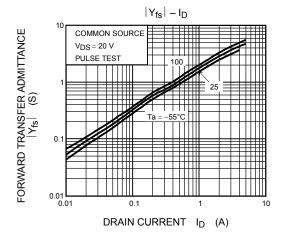


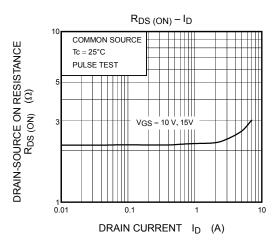


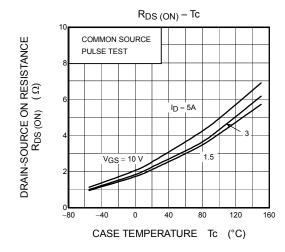


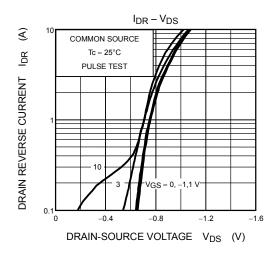


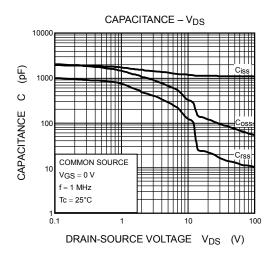


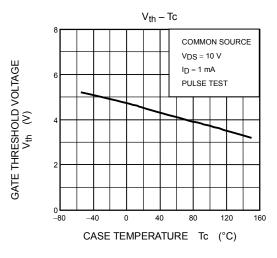


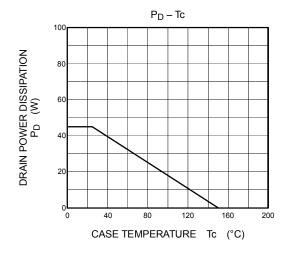


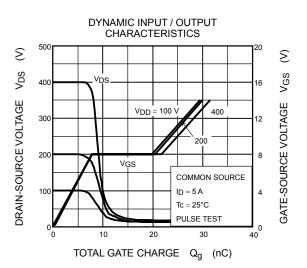




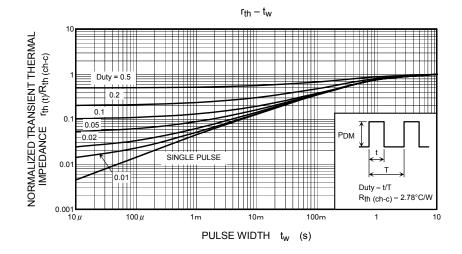


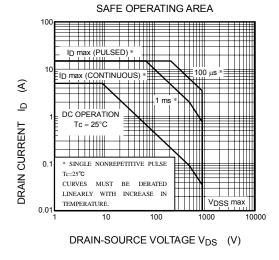


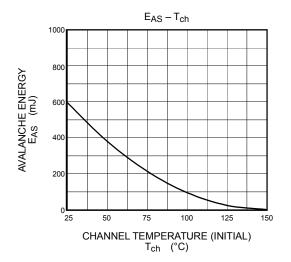


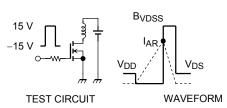


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 43.6 mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot l^2 \cdot \left(\frac{BVDSS}{BVDSS} - V_{DD} \right) \end{aligned}$$

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